

### **REMARKS**

This Amendment is in response to the Office Action mailed on April 21, 2005. Claims 1 and 14 have been amended to include the limitation that "an instruction is inputted to both the high frequency ALU and the low frequency ALU" subsequent to the term "wherein". This limitation is supported at page 10, lines 1-3 and 26-34 of the specification. Editorial amendments also have been made to claims 1-26 to fix grammatical and language informalities, as suggested by the Examiner. Claims 1-26 remain pending.

#### **§112 Rejections:**

Claims 1-26 are rejected and objected to under 35 U.S.C. 112 second paragraph. Editorial amendments have been made to claims 1-26 to overcome these rejections, as suggested by the Examiner. Applicant respectfully requests Examiner to remove the §112 rejections and objections to claims 1-26.

#### **102(b) Rejections:**

Claims 1 and 14 have been rejected as anticipated by Carnevale (US 3,656,123). Applicants respectfully traverse this rejection.

Carnevale fails to discuss, nor teach or suggest at least a computer system comprising "a high frequency ALU driven by the high clock frequency" and "a low frequency ALU driven by the low clock frequency," as required by claim 1. The Office Action on page 13 cites numerous passages of the Carnevale patent (column 2, line 68-column 3, line 6; column 3, lines 16-24 and 31-52; column 3, line 70-column 4, line 14; column 8, lines 21-35; column 24, lines 12-16, 35-58, and 65-69; column 24, lines 1-39; and Figures 1, 3 and 5), none of which discuss the use of another ALU in conjunction with ALU 20.

Carnavale discusses "the timing of the system operation is controlled by means of a plurality of substantially identical system clocks 35 (i.e., 35-1 to 35-n) and a master oscillator 36." which, "[d]epending upon the type of word being executed, the cycle length of the system clocks will be set during the execution of that particular control word." (Column 8, lines 21-35) Thus, Carnevale discusses a plurality of clock signals having different frequencies inputted selectively into ALU 20.

Moreover, Carnevale does not relate to having "an instruction ... inputted to both the high frequency ALU and the low frequency ALU" whereby, "if the high frequency ALU can execute an execution stage instruction correctly, an execution result of the high frequency ALU is output as an execution result of a pipeline execution stage," and "if the high frequency ALU can not execute the execution stage instruction correctly, an execution result of the low frequency ALU is output as the execution result of the pipeline execution stage instead of the execution result of the high frequency ALU." Thus, claims 1 and 14 are neither anticipated nor even suggested by Carneval.

Claim 14 is related to a method for controlling a pipeline operation in a computer system, and includes limitations tracking those of claim 1 discussed above. Accordingly, claim 14 is allowable for at least the same reasons as claim 1 described above.

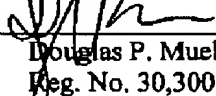
In view of the above amendments and remarks, Applicants respectfully request a Notice of Allowance for claims 1-26. If the Examiner believes a telephone conference would advance the prosecution of this application, the Examiner is invited to telephone the undersigned at the below-listed telephone number.

Respectfully submitted,



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Dated: August 19, 2005

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DPM/ahk